Mithilesh Vaidya

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Education

 Georgia Institute of Technology, Atlanta, USA Master's in Computer Science (Specialisation: Machine Learning) 	[2022 - Present]
 Indian Institute of Technology Bombay, Mumbai, India B.Tech+M.Tech in Electrical Engineering (CPI 9.52/10) with a Minor in Computer Science and Engineering 	[2017 - 2022]
Technical Skills	
 Programming: Python, C++, C, Bash, OpenGL, SQLite, VHDL Softwares/Libraries: PyTorch, LangChain, MATLAB, Arduino, LangX, Blender, AutoCad, Solidworks, Android 	Studio
Professional Experience	
 LLM-powered Natural Language Interface Qualcomm, San Diego — Guide: Mr. Vasudev Nayak, Principal Engineer Machine Lea Designed an end-to-end hierarchical modular pipeline for structured parsing using LLMs such as MPT-7B Developed techniques for reducing bias and hallucinations and compared performance across various open-sou Verification of FPGA-based High Frequency Trading Platform APT Portfolio Pvt. Ltd. — Guide: Mr. Vivek Pannikar, Senior Verification Engineer Implemented Direct Progamming Interface, a protocol for exchanging data between SystemVerilog and C, for verification of testbenches using Cocotb, Quartus and Riviera by 3x Used Python metaclasses for automatically generating Python, SystemVerilog and C DPI header and implementation files from high-level JSON inputs 	Apr'20 - June'20 eering Internship for speeding up Sep'18 - May'19 Co-Founder for segmenting orientation, etc.
Projects and Publications	A 100 MA 100
 Guide: Prof. Vivek Sarkar, GeorgiaTech Implemented compiler optimization techniques such as unroll and jam, scalar replacement and loop reordering in CUDA for reducing execution time of the core capsule operation Explained the 4x speedup in CUDA execution time by examining the profiler output Prominence Detection and Oral Reading Fluency of Children's Read Speech Guide: Prof. Preeti Rao, IIT Bombay — [Published at ICASSP 2022] Replaced a Random Forest Classifier baseline with a CRNN framework and Sinc convolution for predicting the degree of prominence for each word in children's read speech Exploited phrase boundary labels in various multi-task learning paradigms and POS tags and BERT incorporating complementary lexical information Implemented a Wav2vec2.0-based end-to-end model for oral reading fluency assessment and outperforme operating on hand-crafted features by 0.06 (absolute Pearson) Probed the internal representations for presence of knowledge-based features using linear and MLP probes Interpretable latent VAE embeddings for Neural Population Responses 	ed RFC baseline Sep'22 - Present Research project Il activity
Honors and Awards	
 Institute Silver Medal at IIT Bombay for best academic standing in department (2/70) Undergraduate Research Award (URA03) at IIT Bombay for outstanding thesis contributions Institute Special Mention for Journalism carried out as part of Insight, IIT Bombay's student media body JN Tata Scholarship for pursuing higher education abroad 	[2022] [2022] [2022] [2022]

Key Coursework

- Machine Learning: Automatic Speech Recognition, Advanced ML, Foundations of Intelligent and Learning Agents
- Computer Science: Data Structures and Algorithms, Computer Graphics, Network Security, Operating Systems